

Fig. 1

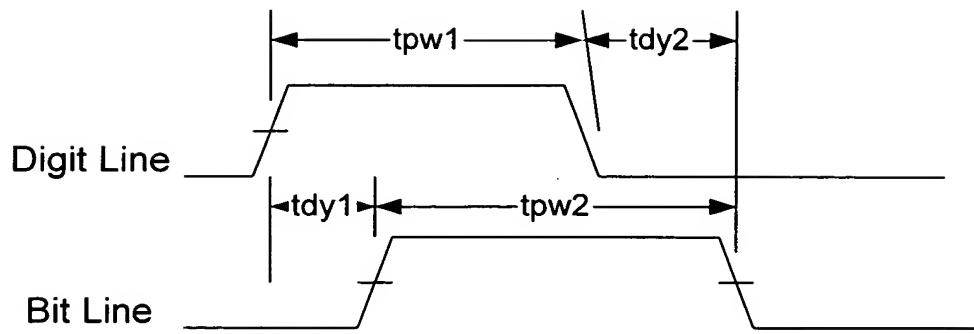


Fig. 2

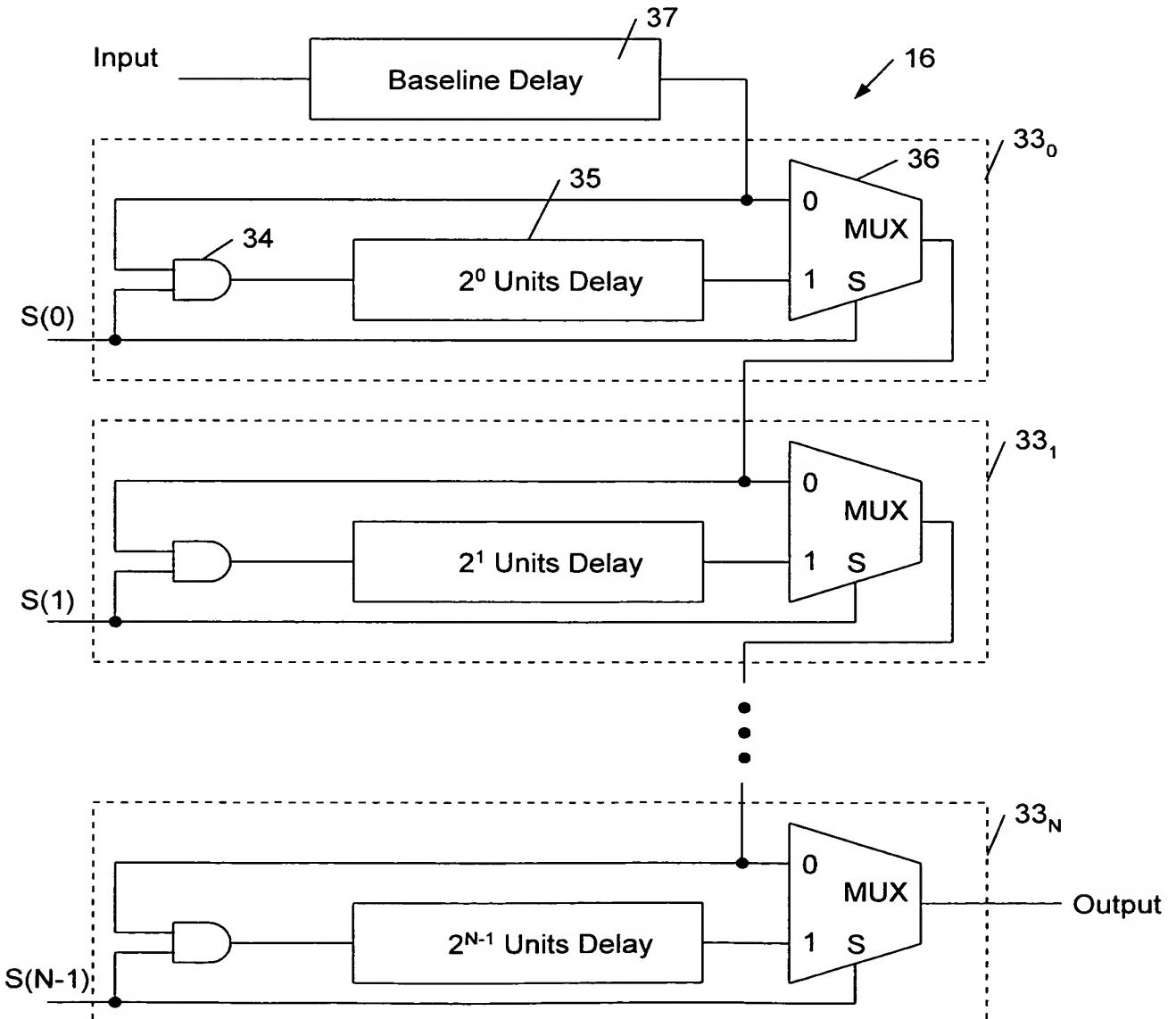


Fig. 3

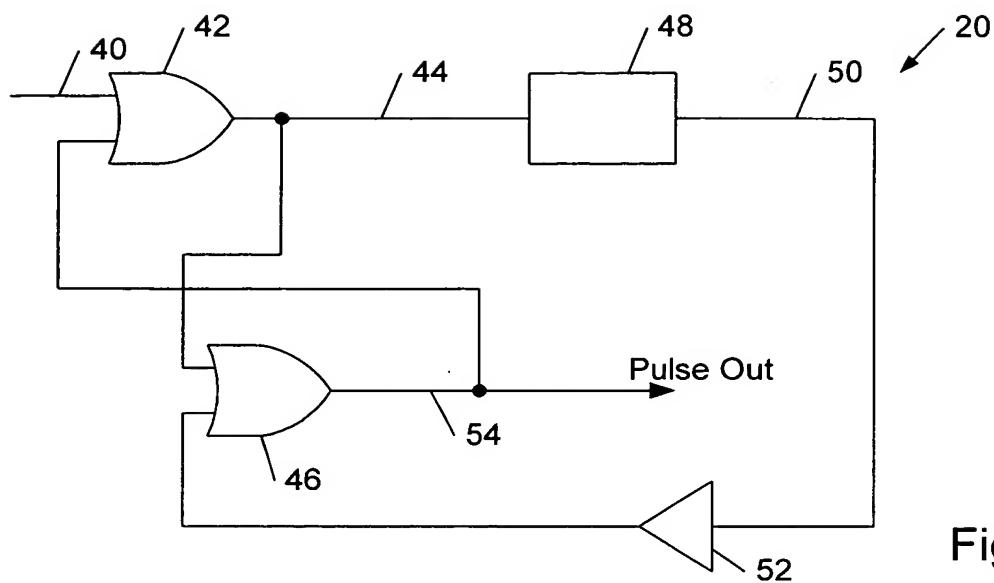


Fig. 4

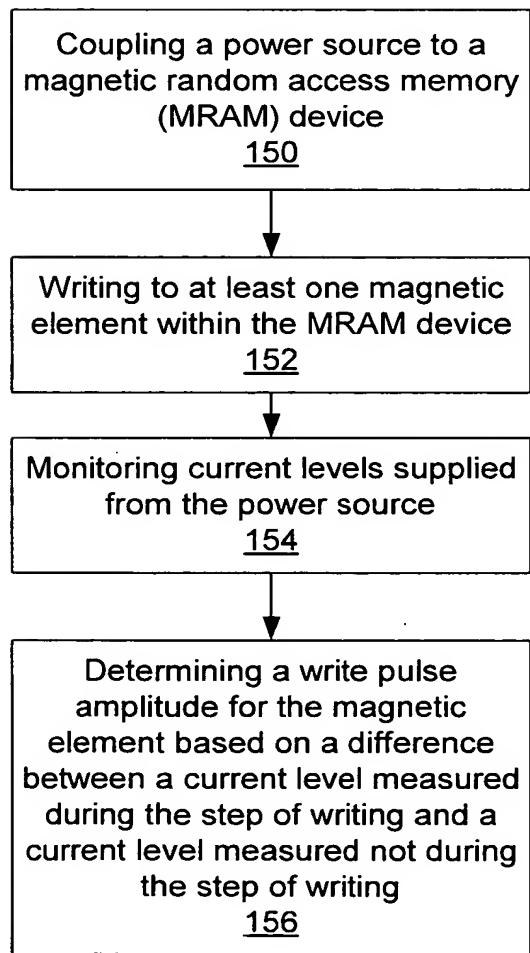


Fig. 5

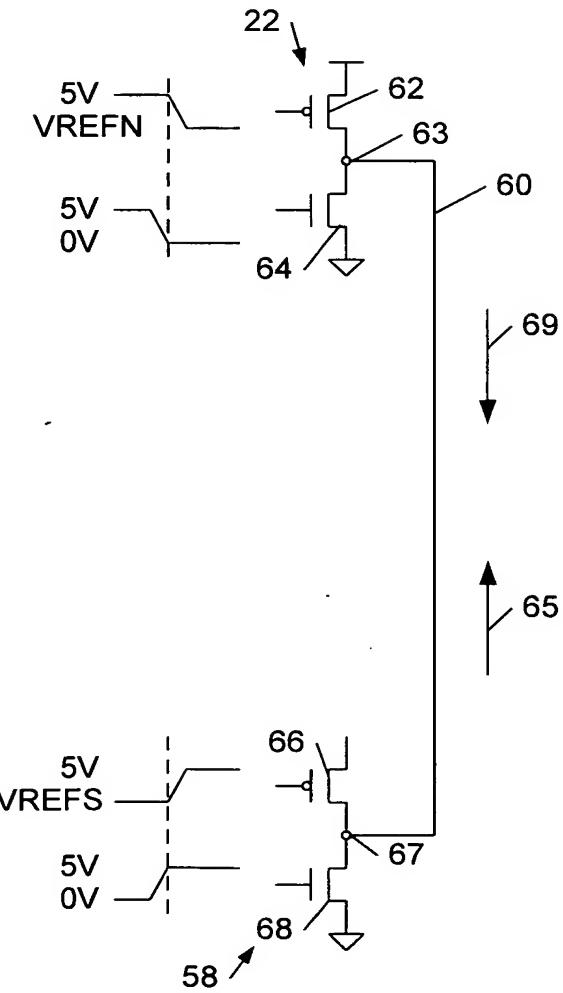


Fig. 6

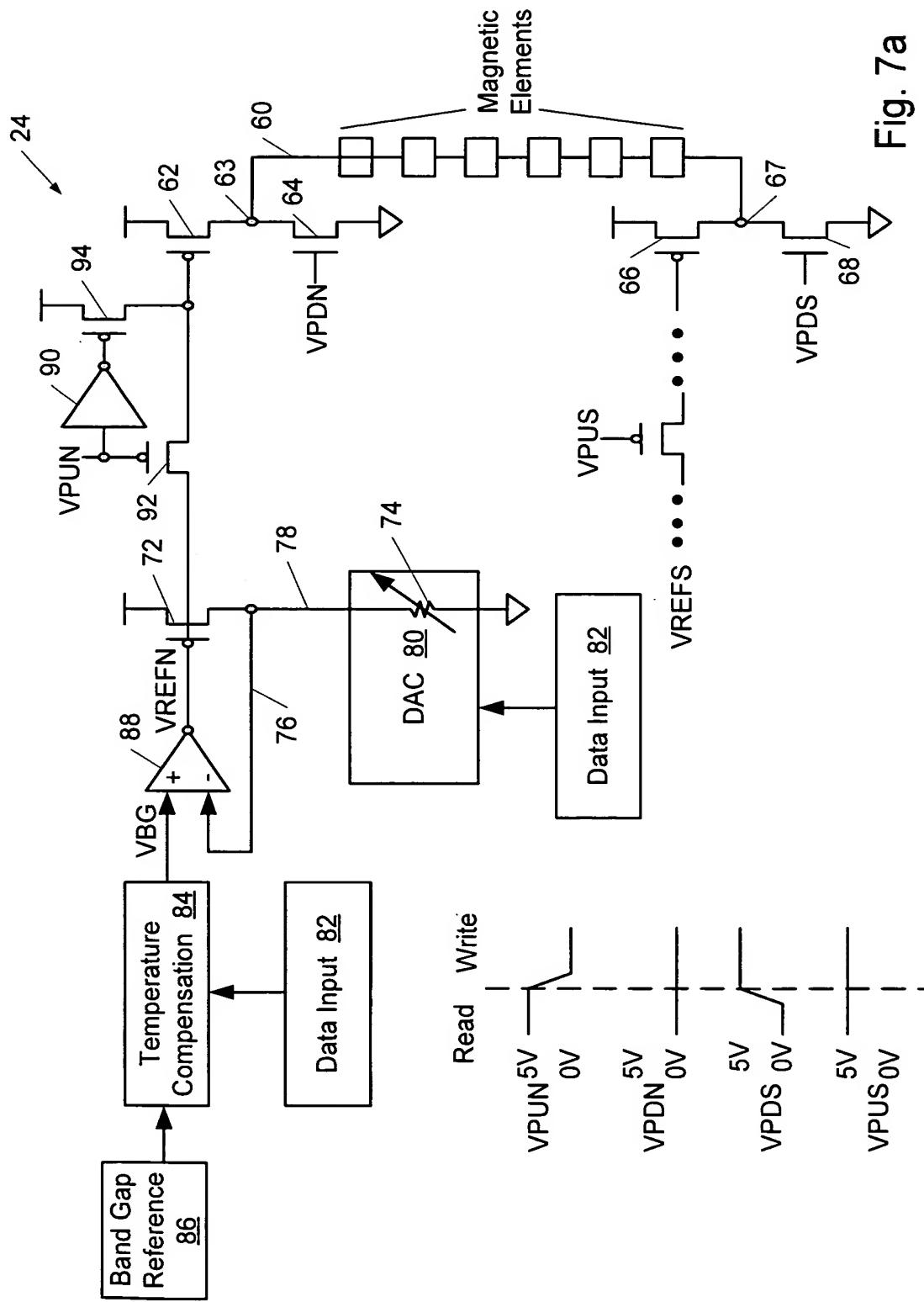


Fig. 7a

Fig. 7b

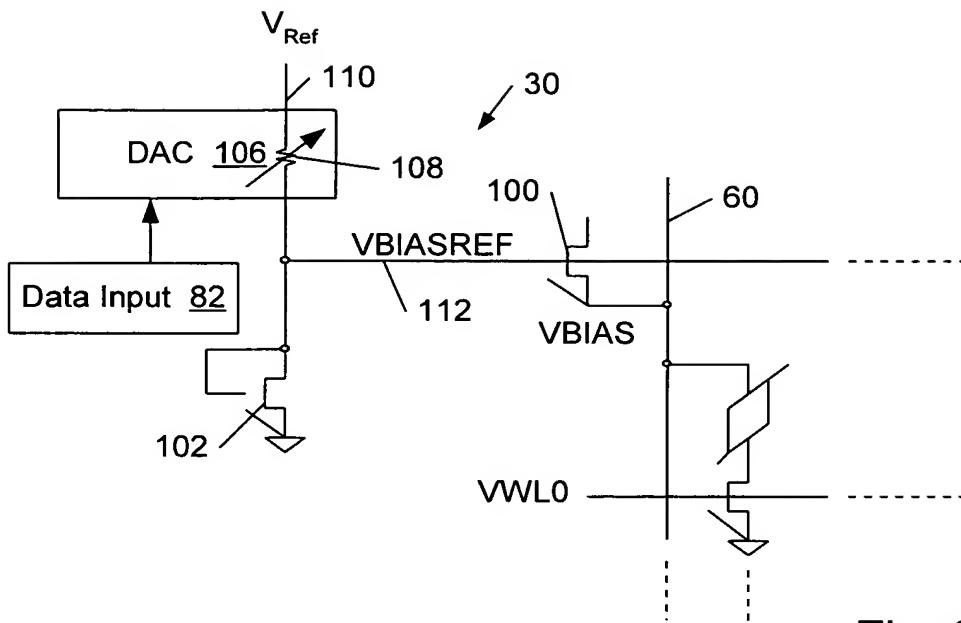


Fig. 8

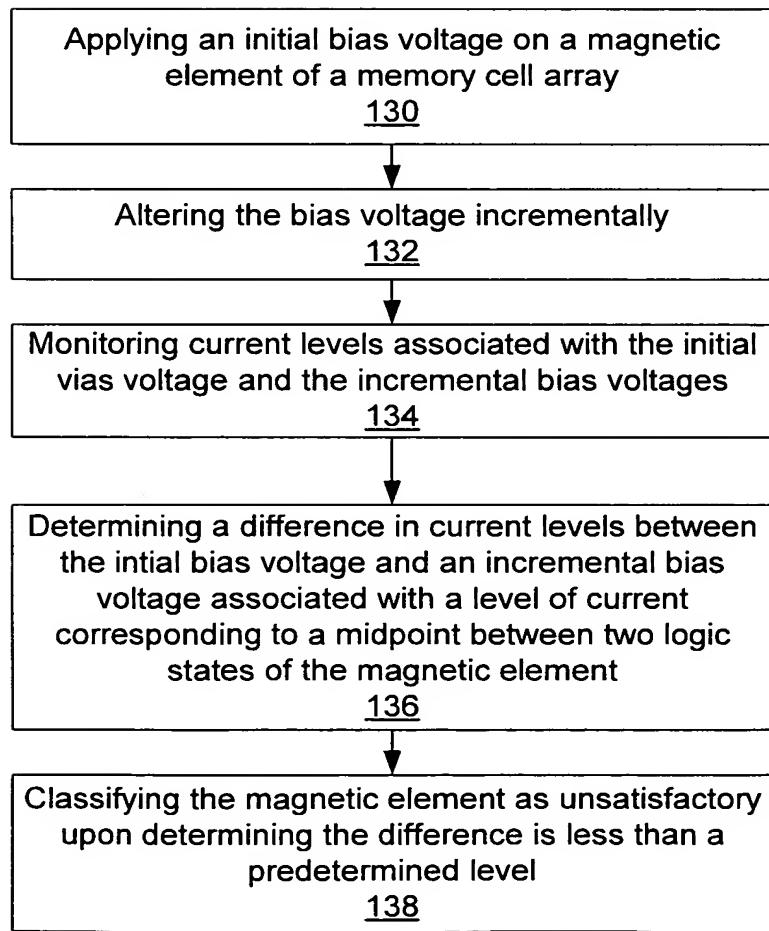


Fig. 10

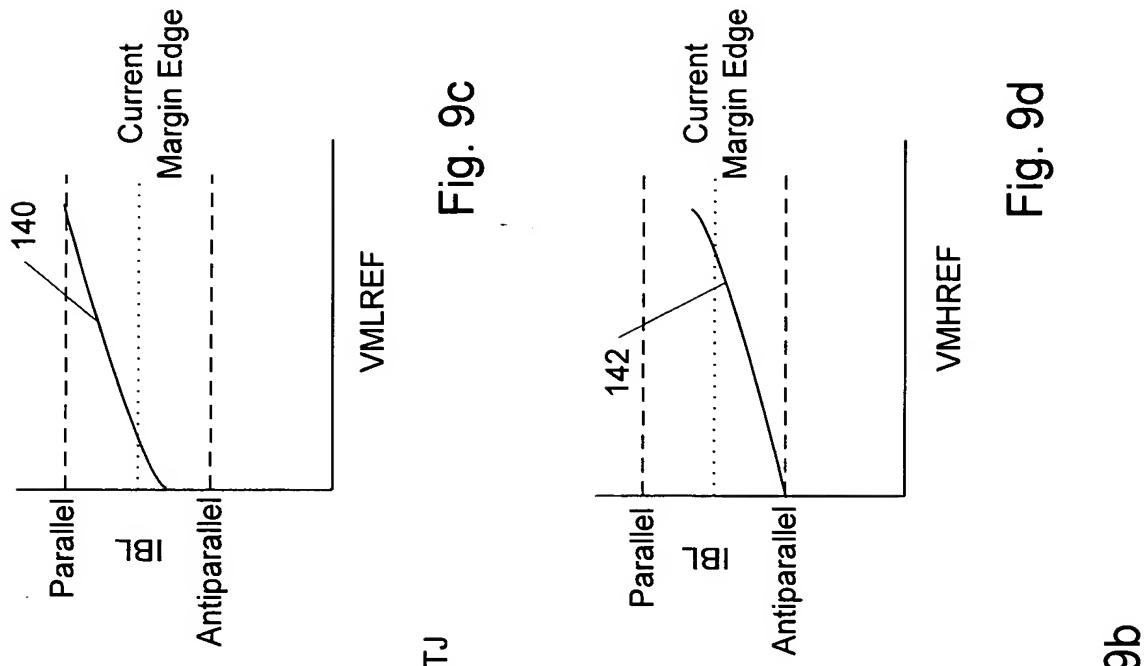
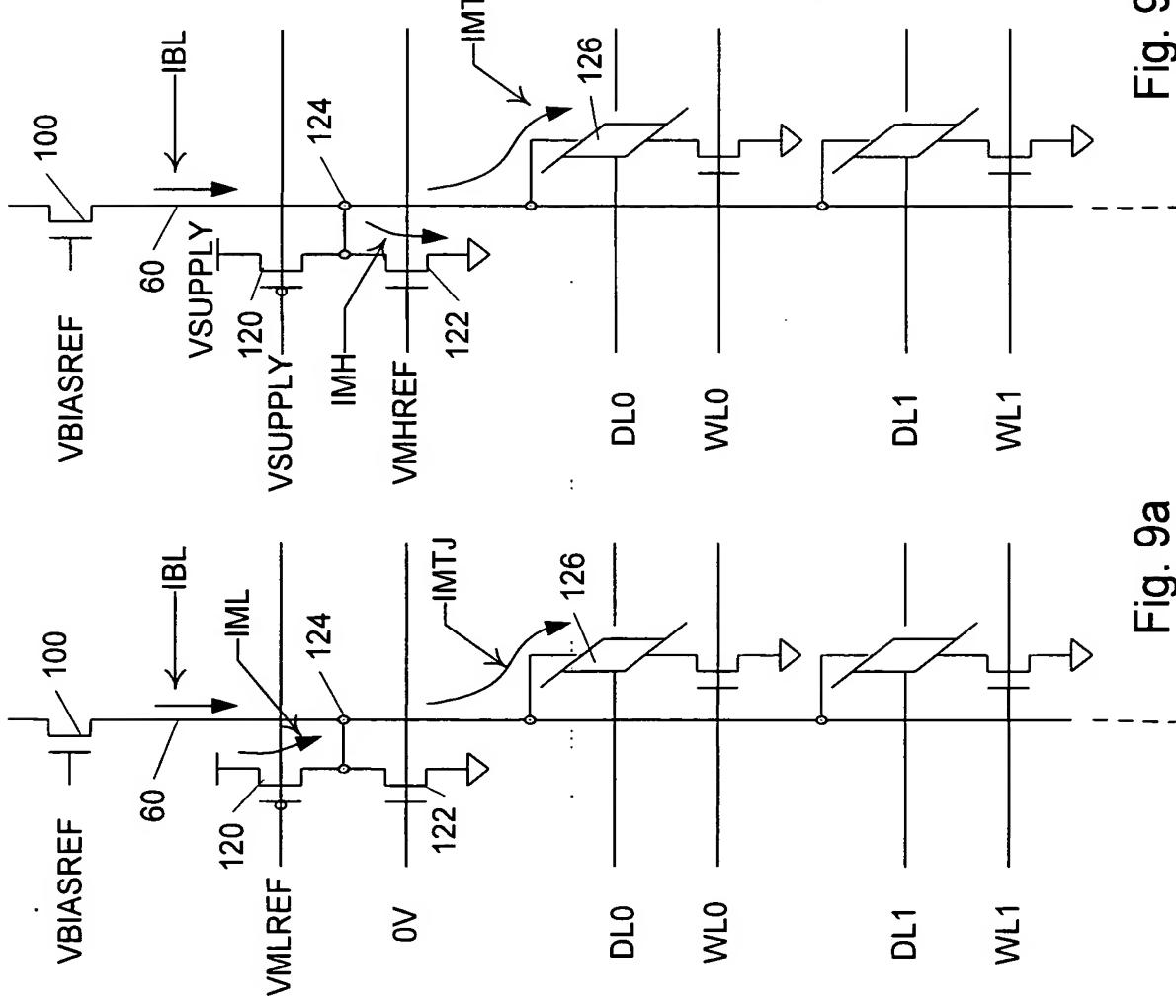


Fig. 9c

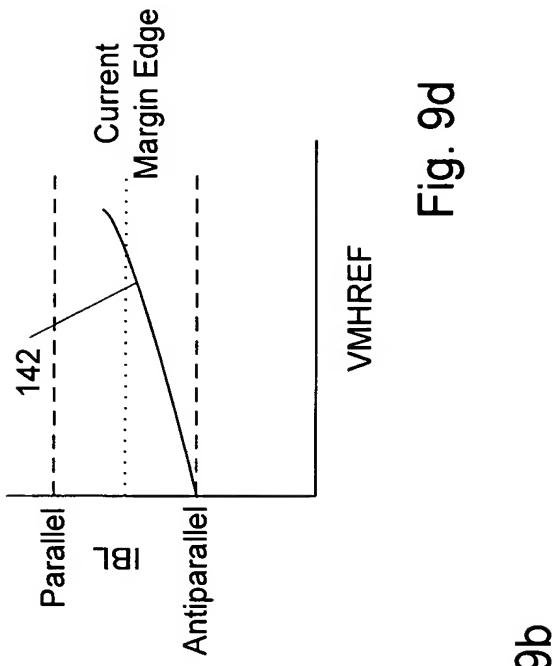


Fig. 9d

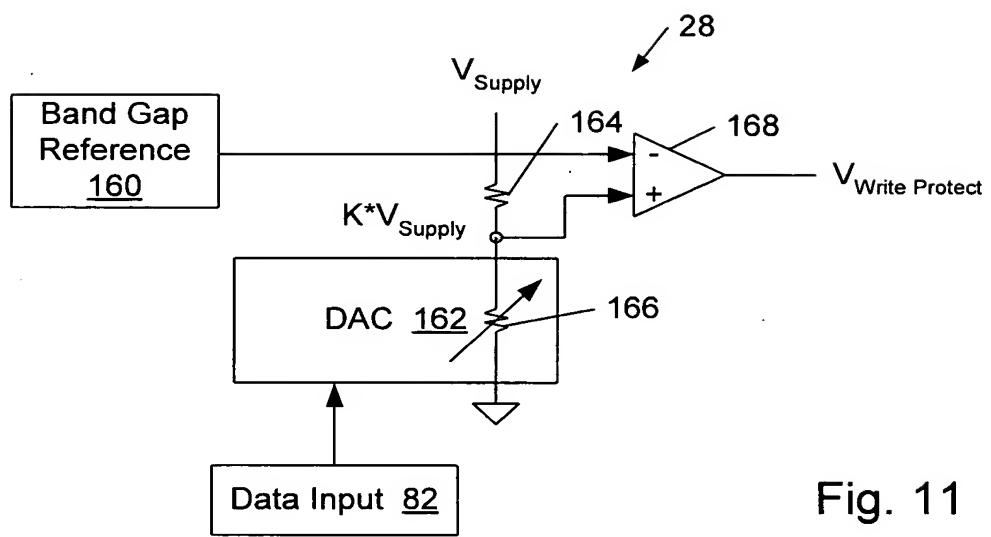


Fig. 11

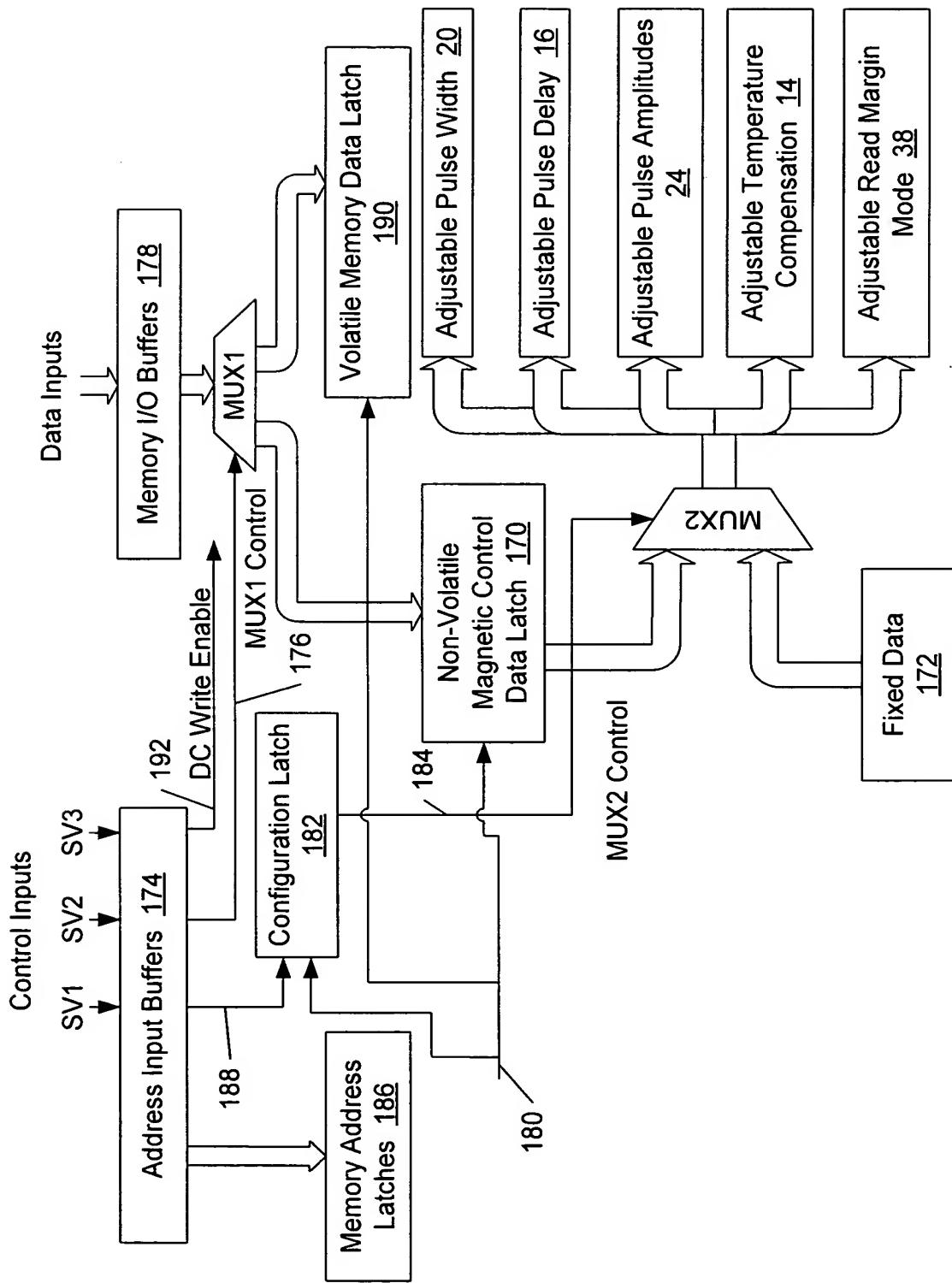


Fig. 12

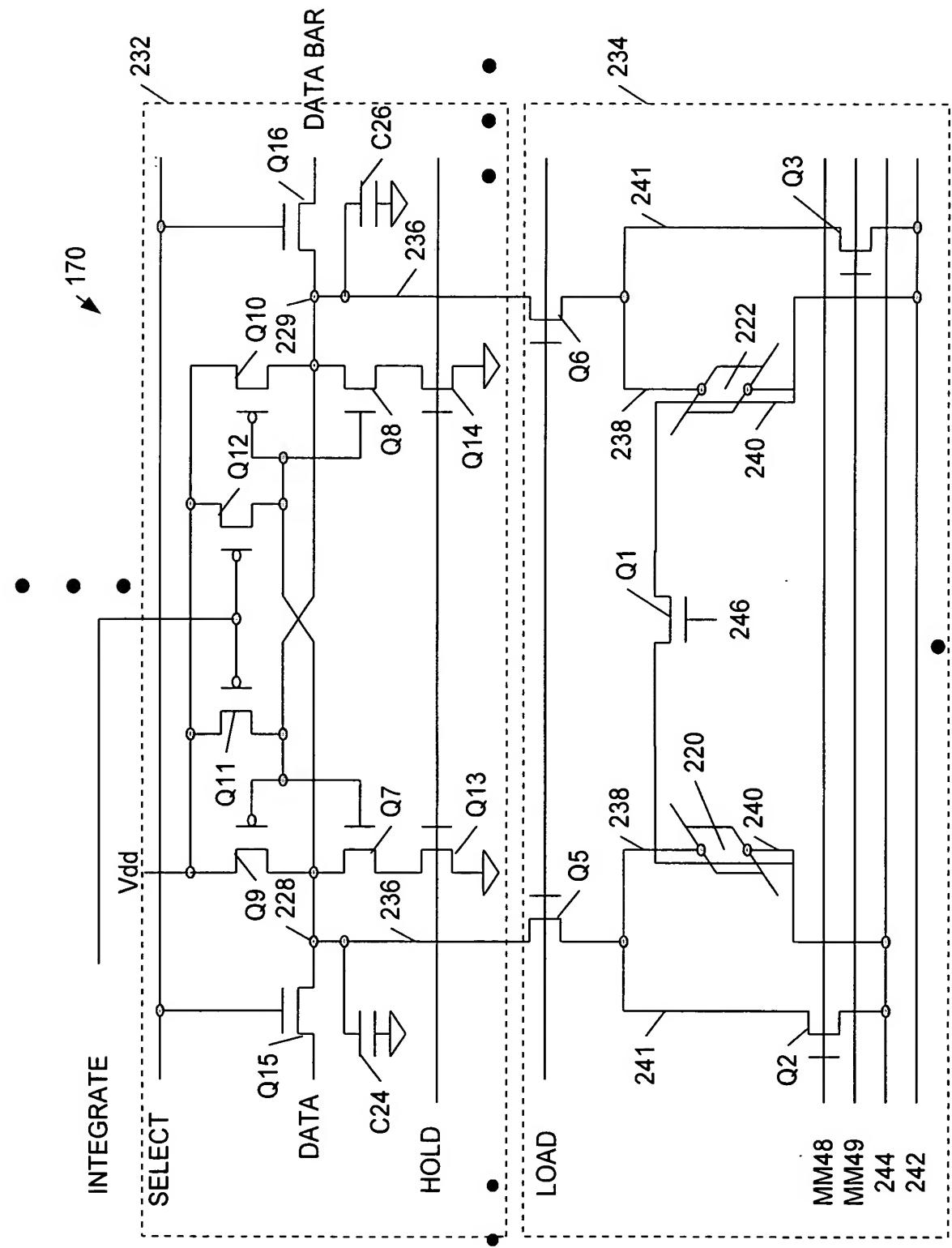


Fig. 13

Charging up first and second nodes  
respectively arranged at opposing ends of  
a volatile portion of a latch

270

Inducing current flow through two  
magnetic elements respectively arranged  
within first and second regions of a  
nonvolatile portion of the latch

272

Loading the first and second nodes to  
have logic states which correspond to  
resistance levels within the magnetic  
elements in the first and second regions,  
respectively

274

Fig. 14

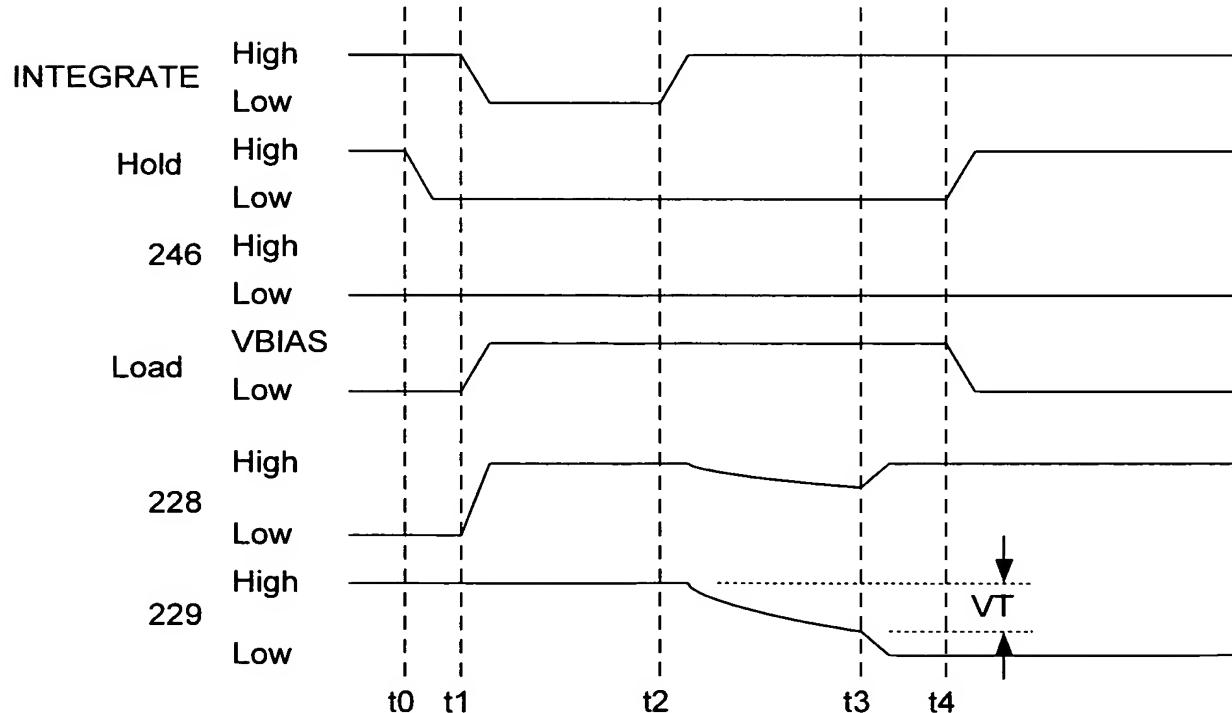


Fig. 15

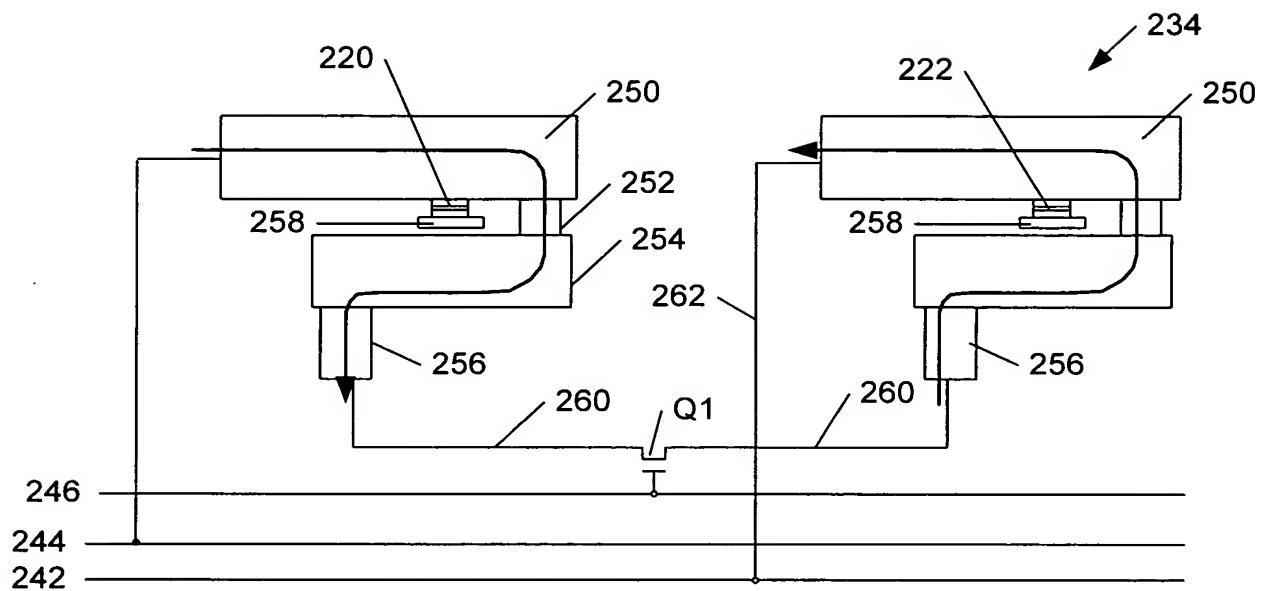


Fig. 16

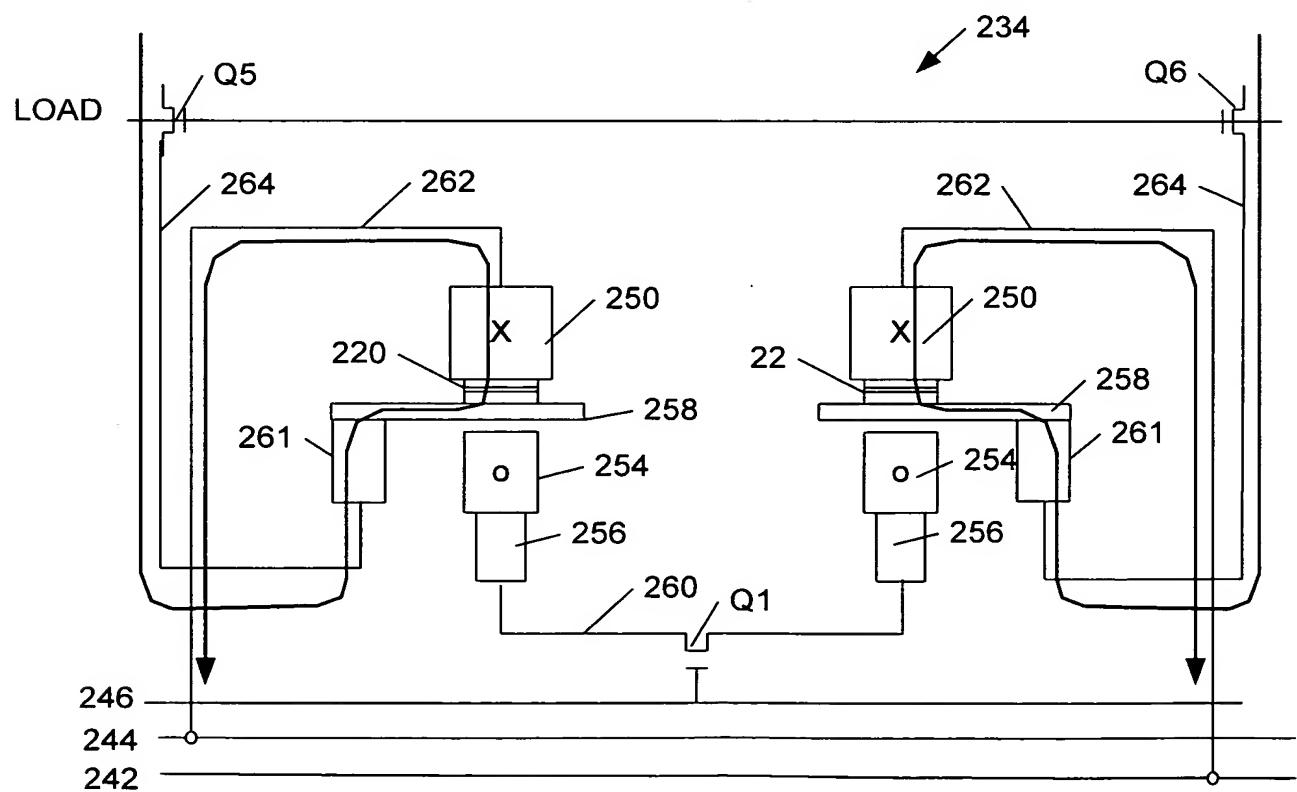


Fig. 17

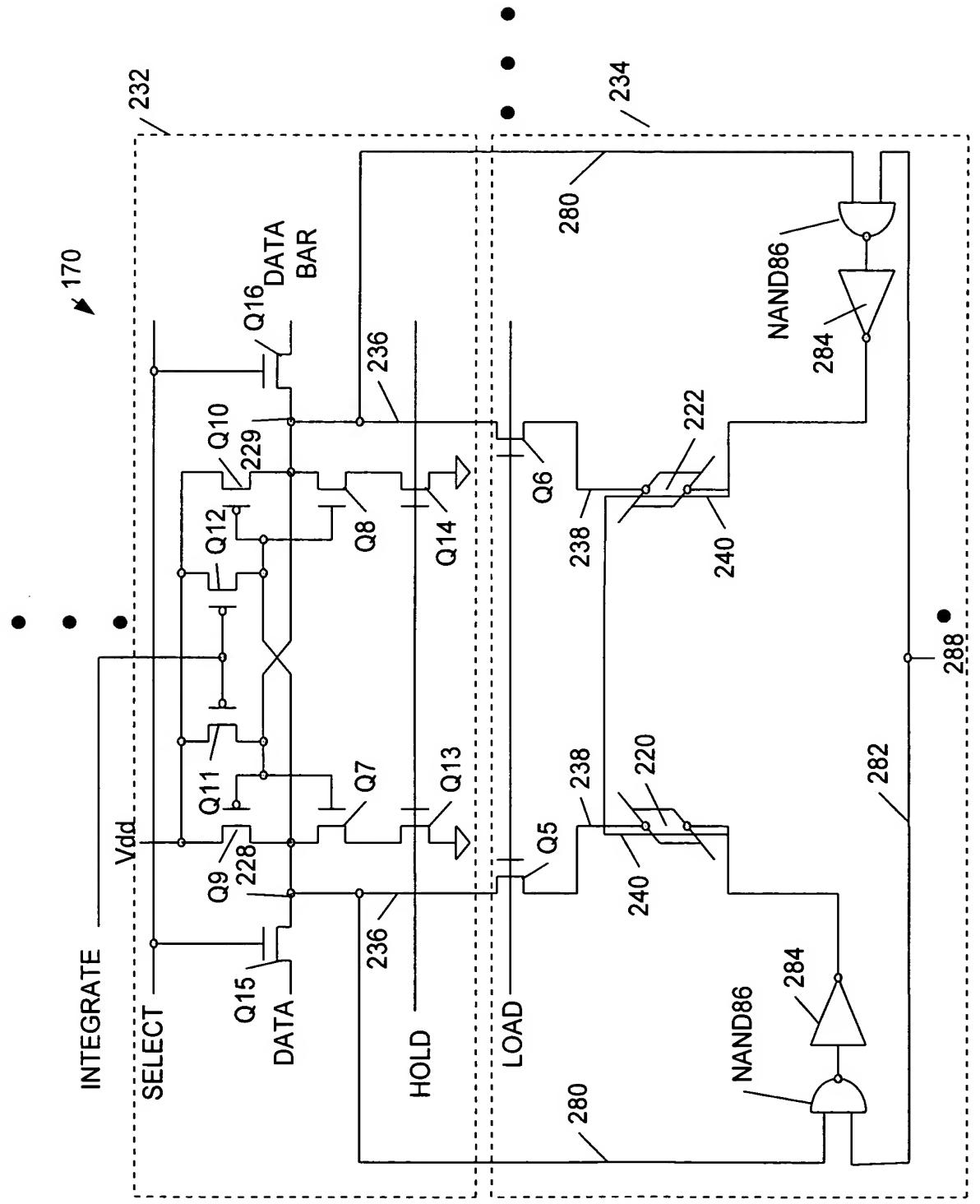


Fig. 18